



Multi State Advanced Manufacturing Consortium
US DOL SPONSORED TAACCCT GRANT: TC23767

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Station Sequence Diagram

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Version 1

Student Name: _____ Station or System: _____

Step #	Step Description	Time (sec)																		Output and Address	Trigger and Address
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18		
1																					
2																					
3																					
4																					
5																					
6																					
7																					
8																					
9																					
10																					
11																					
12																					
13																					
14																					
15																					
16																					

Notes: Remember that the sequence diagram starts with Cycle Start and does not reflect permissives.

[imst_att25.2_sequence_diagram_master_v1_20160318](#)

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