

Multi-State Advanced Manufacturing Consortium

RELEASE DATE VERSION PAGE

3/18/2016

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US DOL SPONSORED TAACCCT GRANT: TC23767 PAGI PRIMARY DEVELOPER: Glenn Wisniewski – Henry Ford College

Integrated Manufacturing Systems Troubleshooting AMTEC PLC Logic Analysis Exercise

Date:				
nstructions: Using the Electrical pri addressing handout, complete the f		prints, the Tag listing pr	ints and the AMTEC Jun	ction box
From operating the AMTEC Trainer, the cycle start finger button has to button has to but of the Amage and an Analog signal to seferom analysis of the PLC logic, trace ogic subroutine and the rung that reshat you took. Show work below:	be initiated. This will initia et speed.) e backwards from the VFD	te the VFD drive on the forward signal to these	simulator. (Forward signs sinputs, when complete	nal to the







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