

## Multi-State Advanced Manufacturing Consortium

RELEASE DATE VERSION PAGE

2/22/2016

v 001 1 of 2

US DOL SPONSORED TAACCCT GRANT: TC23767
PRIMARY DEVELOPER: Jim Blair – Henry Ford College

# **Solid State Electronics – Unit 9: Transistors** *Transistor Questions*

- 1. What conditions must be met for a transistor to be in the cutoff region?
- 2. What conditions must be met for a transistor to be in the saturation region?
- 3. What conditions must be met for a transistor to be in the active region?
- 4. What is the benefit of using the capacitor across the emitter resistor?
- 5. What happens if a load is placed on the common emitter?
- 6. What is the purpose of all the capacitors in the common emitter amplifier circuit?
- 7. How can a transistor be tested?
- 8. If the collector resistor happened to open what would happen to the output voltage if there were no load?







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**Transistor Questions** 

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