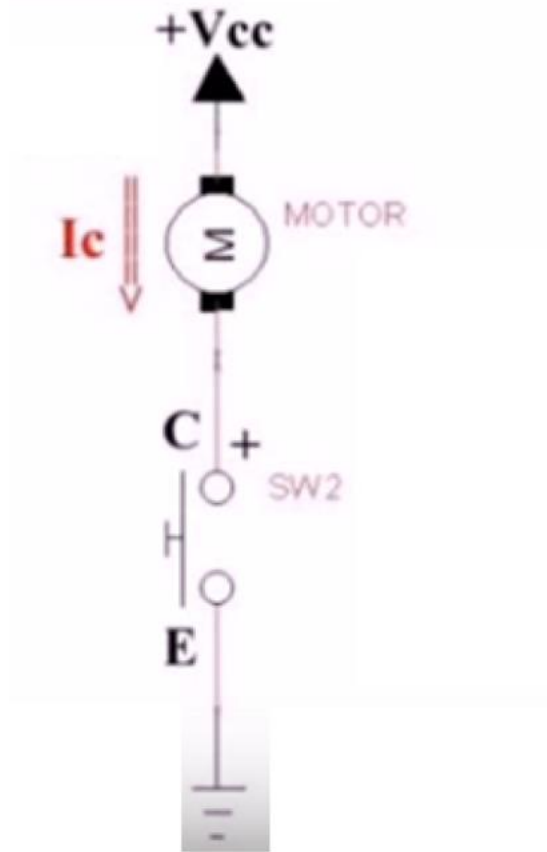


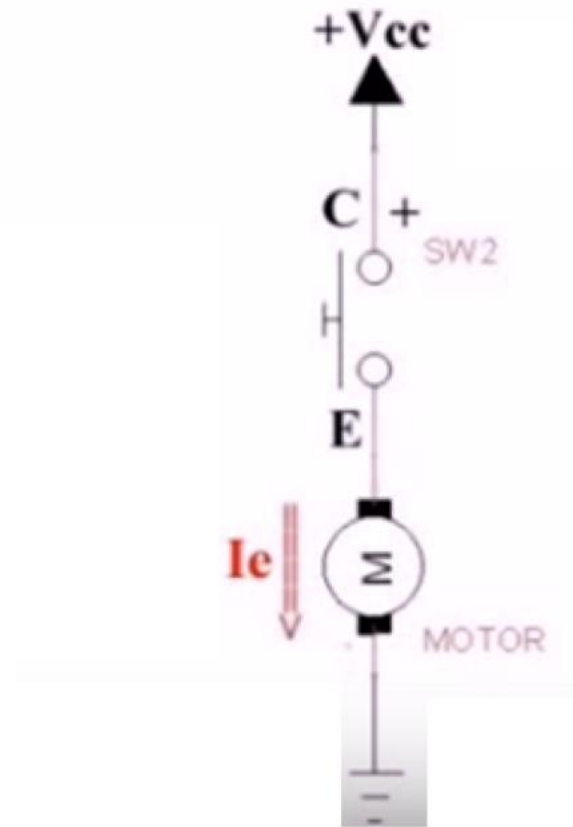


Solid State Electronics – Unit 10: Source or Sink

Source or Sink Diagram



Source



Sink



**Multi-State
Advanced Manufacturing
Consortium**

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PRIMARY DEVELOPER: Jim Blair – Henry Ford College

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Source or Sink Diagram

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