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Iowa Lakes Community College is committed to ensuring that all programs and services, including electronic and our website (www.iowalakes.edu), are accessible to people with disabilities. In accordance with the provisions of Sections 504 and 508 of the Rehabilitation Act and the Americans with Disabilities Act (ADA), Iowa Lakes provides students, faculty, staff, and visitors with reasonable accommodations to ensure equal access to the programs and activities of the college. For more information visit: <https://www.iowalakes.edu/educational-counseling-services/accommodations-disability-resources>.

Mike Gengler is Wind Energy and Turbine Technology Program Coordinator at Iowa Lakes Community College.

Updated in 2017, this course covers an introduction to digital circuits and systems offered in credit programs in a face-to-face format.

Course Syllabus

Digital Circuits & Systems

ELT – 309

Fall/2017

Iowa Lakes Community College
300 South 18th Street
Estherville, IA 51334

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Office Hours: As posted on office door

Catalog Description:

This course provides students with knowledge and understanding of digital logic circuit design and operation using integrated circuits. Studies include combinatorial logic circuits, flip-flops, arithmetic circuits, counters and registers, memory devices and logic families.

This workforce solution is funded in part by the IHUM Consortium which is 100% financed through a \$15,000,000 grant from the U.S. Department of Labor's Employment & Training Administration.

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Prerequisites: ELE-119 Basic Electricity I

C0-requisites: ELE-136 Basic Electricity II

Credits: 3

Text & Additional Materials:

Digital Electronics (Delmar Cengage Learning)
Robert K. Dueck & Kenneth J. Reed

Course Schedule/Outline (Units of Instruction):

1. Digital Electronics
2. Numbers We Use in Digital Electronics
3. Logic Gates
4. Combining Logic Gates
5. Integrated Circuit Specifications and Simple Interfacing
6. Encoding, Decoding, and Seven-Segment Displays
7. Flip-Flops
8. Counters
9. Shift Registers
10. Arithmetic Circuits
11. Memories
12. Simple Digital Systems
13. Computer Systems
14. Analog Devices

Course Objectives:

Upon successful completion of the course, a student should be able to:

- Identify the characteristics of digital circuits as opposed to analog circuits.
- Differentiate between digital and analog signals.
- Identify the HIGH and LOW portions of a digital waveform.
- Classify the signals (analog or digital) in application circuit.
- Explain why converting analog inputs (currents and voltages) from sensors to digital form can be useful.

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- Explain the terms HIGH, LOW, and undefined when observing logic levels in both transistor- transistor logic (TTL) and complementary metal-oxide semiconductor (CMOS) digital circuitry.
- Demonstrate the use of common lab instruments.
- Explain the idea of place value in decimal, binary, octal, and hexadecimal number systems.
- Demonstrate how to convert binary numbers to decimal and decimal numbers to binary.
- Explain the use of terms encode, encoder, decode, and decoder in digital electronics.
- Demonstrate how to convert hexadecimal numbers to binary, binary to hex, hex to decimal, and decimal numbers to hexadecimal.
- Demonstrate how to convert octal numbers to binary, binary to octal, octal to decimal, and decimal numbers to octal.
- Memorize the name, symbol, truth table, function, and Boolean expression for the seven basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR).
- Design a logic diagram from a truth table by first developing a minterm Boolean expression and then drawing the AND-OR logic diagram.
- Analyze the operation of simple logic gate applications.
- Troubleshoot a simple logic circuit.
- Recognize logic gate symbols used in dependency notation (Institute for Electrical and Electronic Engineers standard 91-1984).
- Construct logic diagrams from minterm and maxterm Boolean expressions.
- Explain the basics of current sourcing and current sinking when using TTL ICs.
- Demonstrate how to convert decimal numbers to binary-coded decimal (BCD) code and BCD to decimal.
- Describe the construction of LED displays.
- Test the operation of a 7-segment LED display.
- Construct a 7447 decoder/driver circuit with a common-anode 7-segment LED display.
- Explain the function of each input and output on the R-S flip-flop.
- Explain the modes of operation for an R-S flip-flop.
- Analyze and explain the operation of a latched encoder-decoder system.
- Detail the operation of counter circuits.
- Construct a serial-load shift register circuit using D flip-flops.
- Define terms used to describe modes of operation for a shift register (such as shift right, shift left, serial loading, parallel loading, hold).
- Define characteristics of shift registers.

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Methods of Instruction: Course will consist of a two-hour lecture, once each week, which may include reviewing assignments, discussion, demonstrations and other methods to be determined by the instructor. Two hours of labs meeting once each week will follow the lecture and will cover material discussed during that week. Labs will consist of students performing electrical drawings, wiring and troubleshooting exercises as well as other methods determined by the instructor.

Grading Scale:

- A = 90-100%
- B = 80-89.999%
- C = 70-79.999%
- D = 60-69.999%
- F = 0-59.999%

Other Expectations: Students are expected to attend all classes and labs. Students unable to attend a lecture or exam must notify the instructor by e-mail before the absence. Students who are absent and have notified the instructor will be allowed to reschedule tests or assignment. Students who are absent and have not notified the instructor will not receive credit on any test or assignment due that day. Students are responsible for learning the course material covered during their absence. The instructor will not notify students individually if assignments or deadlines are missed. Each exam will be announced at least one classes in advance. Students who leave the room while a test is in progress must submit their exam as completed. The final is comprehensive and will be administered during finals week. Students must have achieved a grade of “D” or higher to be eligible for the final exam. Students are expected to conduct themselves in a professional manner. Classes and labs will start promptly. If you are unprepared do not enter the classroom. All communications must be conducted through the iowalakes.edu e-mail address. Any behavior which is disruptive or unsafe may be grounds for removal from class. Cell phone use and texting are prohibited in lecture. Cell phone use and texting are prohibited in lab, unless instructed otherwise. The use of a cell phone will result in a zero for anything done on that day. Only one warning will be given. Students who need to leave class early should let the instructor know before class begins and leave quietly. Avoid prolonged noise, especially during class discussions. Incompletes are only issued when the student can establish a completion date. Drug and alcohol use is prohibited. Students suspected of being under the influence of drugs or alcohol will be asked to leave.

Important: NO food or drinks in the lab.

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Students must abide by all policies as stated in the Iowa Lakes Community College Student Handbook.

Students should be aware that classes might be audio or video recorded by one or more students. The college's policies governing the audio or video recording of class are included in the Student Handbook. Students who have any questions or concerns about class recordings should address **their questions or concerns with the instructor at the *beginning of the semester.***

STUDENT ACADEMIC HONESTY POLICY

Iowa Lakes Community College believes that personal integrity and academic honesty are fundamental to scholarship. Iowa Lakes strives to create an environment where the dignity of each person is recognized and an atmosphere of mutual trust exists between instructors and students. The faculty has confidence in the integrity of the students and encourages students to exercise good judgment in fulfilling this responsibility.

Actions contrary to academic integrity will not be tolerated. Activities that have the effect or intention of interfering with learning or fair evaluation of a student's work or performance are considered a breach of academic integrity. Examples of such unacceptable activities include, but are not limited to:

- **Cheating** (intentionally using or attempting to use unauthorized material, assistance or study aids in my academic work). For example, using a cheat sheet for a test, looking at another student's paper during an exam, stealing or buying all or parts of an exam or paper, altering and resubmitting work for a better grade without prior approval to do so, etc.
- **Plagiarism** (representing another's ideas, words, expressions or data in writing or presentation without giving proper credit, failing to cite a reference or failing to use proper documentation, using works of another gained over the Internet and submitted as one's own work).
- **Falsification and/or misrepresentation of data** (submitting contrived or made-up information in any academic exercise). For example, making up data, citing non-existent sources, etc.
- **Facilitating Academic Dishonesty** (knowingly helping or attempting to help another violate any provision of the academic honesty policy). For example, working together on a take-home exam or other assignment when the option has not been made available, giving a paper/assignment to another student for his/her use, etc.
- **Multiple Submissions** (submitting, without prior approval from the instructor involved, any work submitted to fulfill academic requirements in another class). For example, submitting the same paper for two different classes, etc.
- **Unfair Advantage** (trying to gain unauthorized advantage over fellow students). For example, gaining or facilitating unauthorized access to exam materials (past or present); interfering with another student's efforts in an academic exercise; lying about the need for an extension on a paper or assignment; destroying, hiding, removing or keeping library materials, etc.

Disciplinary Action

Any violation of this policy will be treated as a serious matter. The instructor has primary responsibility over classroom behavior and maintaining academic integrity. Students who earn an "F" based on any violation of the Student Academic Honesty Policy may not withdraw from the class (and receive a grade of W). Depending on the nature and severity of the offense, Iowa Lakes Community College reserves the right to exercise disciplinary action as outlined in the Disciplinary Action Section of the Student Handbook.

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Americans with Disabilities Act – Policy of Nondiscrimination

It is Iowa Lakes Community College policy to not discriminate against qualified individuals with disabilities and to provide reasonable accommodation(s), as required by law, to otherwise qualified applicants for admission or to students with disabilities in all education programs, activities, services and practices, including application procedures, admissions, course selection, the awarding of degrees, discipline and dismissal. Educational opportunities will not be denied to an otherwise qualified application or student because of the need to make reasonable accommodation(s) or modification(s) for the physical and mental impairment(s) of any such individual.

Iowa Lakes Community College students needing reasonable accommodation(s) and/or modification(s) should contact Jody Condon by phone at (712) 852-5219 or via email at jcondon@iowalakes.edu. To assure that accommodation(s) and/or modification(s) will be ready when classes start, students must make the request as soon as possible, before a semester begins.

It is the policy of Iowa Lakes Community College not to discriminate on the basis of sex, race, national origin, creed, age, marital status or disability in its education programs, activities, or employment policies, as required by Titles VI and VII of the 1964 Civil Rights Act, Title IX of the 1972 Educational Amendments, Section 504 of the Federal Rehabilitation Act of 1973 and Title II of the Americans with Disabilities Act (ADA) of 1990.

Inquiries regarding compliance with Title IX, Title VI, Title VII, or Section 504 may be directed to Kathy Muller, Human Resources, Iowa Lakes Community College, 19 S. Seventh Street, Estherville, IA 51334, telephone (712) 362-0433; to the Director of the Iowa Civil Rights Commission, Des Moines; or to the Director of the Region VII Office of Civil Rights, Department of Education, Kansas City, Missouri.

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